The SiPeX workshop aims to bring together reliability and test engineers to discuss advanced design methodologies, integration technology and assembly engineering for SiP solutions that embrace heterogeneity, multiple energy domains and mixed technology platforms. Papers are invited that address both current challenges and those expected in the future in the following areas:

- Embedded test, Design-for-test and novel test strategies
- Reliability analysis and modeling techniques, FMEA and Physics of failure
- Identification, characterization, and modeling of defects, fault and degradation mechanisms
- Reliability measurement and life testing strategies, test structures design, fabrication and characterization.
- On-line test, health monitoring fault tolerance and self-repair
- Emerging approaches for Design for Reliability and Design for Yield.
- Robust and dependable design strategies, solutions for harsh and challenging environments
- Design for Disposal, Safety Engineering (use of materials and impact on systems)

The organising committee invites authors to submit both papers and poster contributions in the above areas. Submissions should consist of either an extended summary of at least 750 words or a full paper. Authors of accepted submissions will be encouraged, though not obliged, to prepare a final version of their paper or poster to be included in the informal workshop proceedings.

The Workshop will take place at the Grand Hotel Majestic, Pallanza, Lago Maggiore, Authors should submit their contributions to nouet@lirmm.fr by the submission date below. Notification will be sent out by April 14th 2008.

**Submission deadline:** Monday, March 3rd, 2008