



EBTW05

IEEE European Board Test Workshop 25 – 26 May, 2005 Reval Hotel Olümpia, Tallinn, Estonia Call for Papers

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Following the successful US-based Board Test Workshops (BTWs), the BTW Organizing Committee is organizing a 1-day European BTW event in association with the 2005 European Test Symposium. EBTW05 will focus on current issues and trends related to board test. Similar to earlier BTWs, perspectives are invited from contract manufacturers, test equipment providers, researchers, end users and systems providers.

EBTW05 is sponsored by the IEEE Computer Society - Test Technology Technical Council (TTTC) and supported by the IEEE Board Test Technology Activities Committee (BTTAC). We cordially invite you to participate and submit your contribution to EBTW05, which includes (but is not limited to) the following single-board and multi-board test topics:

Traditional Board Test Techniques

Boundary Scan-based test techniques

Assessing BSDL compliance and accuracy

Board test: structural versus functional test

Parametric testing

On-Board Built-In Self Test techniques

Electrical, Optical and X-Ray board test mix

Board test for high-volume consumer products

Advances in flying probe technology

Creating board functional tests

Advances in ICT fixturing

Interfacing test flows with manufacturing flows

Board test: linking ICT with low-cost PC testers

Board test: fault-coverage metrics

Board test: reducing false fails

Applications of VXI/PXI plug 'n play technology

System-Level and Field-Service Test

Using 1149.1 as a backplane test-bus

1149.1 backplane test-bus support devices

Systems design integration and test issues

Field servicing: test needs and solutions

New Standards and New Problems

Practical use of P1581

Advances in 1149.6 AC-EXTTEST

Advances in 1149.4 Mixed-Signal test Bus

Test techniques for SERDES boards

At-speed board and system structural test

Outsourcing to EMS companies

Outsourcing to EMSs – issues and solutions

Re-using prototype tests in volume manufacturing

In-System Configuration

OnBoard programmability of CPLDs/FPGAs

How to program PLDs to execute test functions

Board and System Test Economics

Board test economics

Emulation in a field-service environment

Board test: educational requirements

Submissions: The Program Committee invites original submissions of at least 1,000 and at most 5,000 words focusing on new concepts and methodologies. Full paper submissions are preferred and recommended but the Program Committee will accept extended abstracts.

Electronic submission in Adobe PDF via the workshop web page (see below) is mandatory. The web site also informs about the *review criteria*. Please identify a contact author with complete information (mailing address, phone number, fax number and e-mail address).

Proceedings: At the event, EBTW05 will deliver a CD-ROM of the accepted contributions whose authors wish to provide the corresponding materials.

Deadlines and Key Dates:

Submission of full papers or extended abstracts: **25 February, 2005**

Notification of acceptance: **15 April, 2005**

Final paper due: **13 May, 2005**

Further Information

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