



# 11th IEEE European Test Symposium

Hilton Hotel, Southampton, United Kingdom  
May 21-25, 2006  
<http://www.ieee-ets.org>



## Call for Submissions

The IEEE European Test Symposium (ETS) is Europe's premier forum dedicated to presenting and discussing scientific results, emerging ideas, practical applications, hot topics, and new trends in the area of electronic-based circuit and system testing. In 2006, ETS will take place in Southampton, the historic port on the south coast of England. ETS'06 is being organized by the University of Southampton and sponsored by the Test Technology Technical Council (TTTC) of the IEEE Computer Society.

You are invited to participate and submit your contributions to ETS'06. The areas of interest of ETS'06 include (but are not limited to) the following topics:

- Analog, Mixed-Signal, RF Test
- ATE Hardware and Software
- Automatic Test Generation
- Board and System Test
- Built-In Self Test (BIST)
- Current-Based Test
- Defect and Fault Tolerance
- Delay and Performance Test
- Design for Test(ability) (DfT)
- Design Verification and Validation
- Diagnosis and Debug
- (Embedded) System Test
- Failure Analysis
- Fault Modeling and Simulation
- High-Level DfT and TPG
- High-Speed IO/Interconnect Test
- Memory Test and Repair
- MEMS and Nanotechnology Test
- Microprocessor Test
- Nanometer Technologies Test
- On-Line and Off-Line Test
- Power Issues in Test
- Self-Repair Methodologies
- Signal Integrity Test
- Standards in Testing
- System-in-Package (SiP) Test
- System-on-Chip (SoC) Test
- Test Data Compression
- Test of Reconfigurable Systems
- Test Quality and Reliability
- Test(ability) Synthesis
- Thermal Test
- Yield Analysis and Enhancement

**Publications** – ETS'06 will produce *Formal Proceedings* of selected papers, published by the IEEE Computer Society, and an *Informal Digest*. A post-conference CD-ROM with papers, presentation slides, and photos will be mailed to all participants. Selected papers will be invited to submit a journal version of their manuscript for inclusion in a Special Issue of the *IEEE Proceedings on Computers and Digital Techniques*, to appear in May 2007.

**Submissions** – ETS'06 seeks original, unpublished contributions of the following types.

- Scientific papers for the Formal Proceedings.
- Workshop-type papers for the Informal Digest, including 'emerging ideas' and 'case studies'.
- Proposals for panels, embedded tutorials, and other special sessions.
- Proposals for full-day tutorials on test-technology topics.
- 'Student Forum' papers for the Informal Digest. BTW, ETS'06 provides financial support to students; students with a paper accepted at ETS'06 (in any submission category) will get priority in the assignment of these grants.
- 'Vendor Session' presentations, for which corresponding white papers can be included in the Informal Digest. Vendor Session submission is open to all, but industrial sponsors of ETS'06 will get preferential treatment in the selection process.

Detailed submission instructions, including selection criteria and publication policies, for the various types of contributions are posted on the ETS'06 web page; electronic submission of PDF files via this page is required. – Note: ETS'06 will present a Best Paper Award at ETS'07.

**Key Dates:**

- Submission deadline : **December 9, 2005**
- Notification of acceptance : **February 11, 2006**
- Camera-ready manuscript : **March 11, 2006**

### Further Information:

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